# EDA Tool Compatibility

* Cadence EDA tools were used for verification and synthesis of this product.
* Incisive RTL Simulator 15.22.012
* Genus RTL Synthesis 15.20-p004\_1
* HAL Linting tool 13.20.036
* Confirmal 15.10.120
* Compatibility testing has been done with VCS J-2014.12-SP3-3. Please refer to IP Integration

specification to enable/disable specific NetSpeed checker in order to resolve or workaround

any verification related issues, if any. Contact your NetSpeed or Synopsys support team

for assistance.